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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

First Named Inventor: OSHIBA, MASASHI

Art Unit:

Intl. Appln. No.: PCT/JP2004/014939

Examiner:

Intl. Filing Date: 08 October 2004

Confirmation No.:

For: SEMICONDUCTOR INTEGRATED CIRCUIT APPARATUS AND
ELECTRONIC SYSTEM

INFORMATION DISCLOSURE STATEMENT

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Attn: Mail Stop PCT, DO/EO/US

Sir:

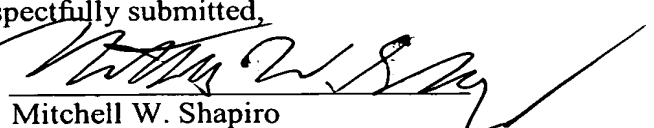
Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO/SB/08A are hereby cited.

Document 3 on the attached List was cited in the specification, on page 2, and its relevance is indicated therein. Document 3 is a counterpart of U.S. Document 1.

Documents 2, 4, and 5 were cited in the International Search Report (copy attached).

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 (T3844-11608US01) any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been separately requested, such extension is hereby requested.

Respectfully submitted,

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For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the
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